



US 20100265166A1

(19) **United States**(12) **Patent Application Publication****Kang et al.**(10) **Pub. No.: US 2010/0265166 A1**(43) **Pub. Date: Oct. 21, 2010**(54) **PIXEL AND ORGANIC LIGHT EMITTING
DISPLAY DEVICE USING THE PIXEL****Publication Classification**(51) **Int. Cl.**
G09G 3/30

(2006.01)

(52) **U.S. Cl.** **345/76**(57) **ABSTRACT**(76) **Inventors:** **Chul-Kyu Kang**, Yongin-city (KR);
Yu-Lian Choi, Yongin-city (KR);
Keum-Nam Kim, Yongin-city (KR)

Correspondence Address:

CHRISTIE, PARKER & HALE, LLP
PO BOX 7068
PASADENA, CA 91109-7068 (US)(21) **Appl. No.:** **12/693,388**(22) **Filed:** **Jan. 25, 2010**(30) **Foreign Application Priority Data**

Apr. 17, 2009 (KR) 1020090033571

A pixel includes an organic light emitting diode; a first transistor; a second transistor coupled to a data line and turned on when a scan signal is supplied to an i^{th} scan line; a third transistor between the second transistor and a gate electrode of the first transistor and turned on when a scan signal is supplied to an $i+1^{th}$ scan line; a fourth transistor between the gate electrode of the first transistor and a reference power supply and turned on when the scan signal is supplied to the i^{th} scan line; a fifth transistor between the organic light emitting diode and an initial power supply and turned on when a control signal is supplied; a first capacitor between the second transistor and a node between the organic light emitting diode and the initial power supply and a second capacitor between the node and the gate electrode of the first transistor.

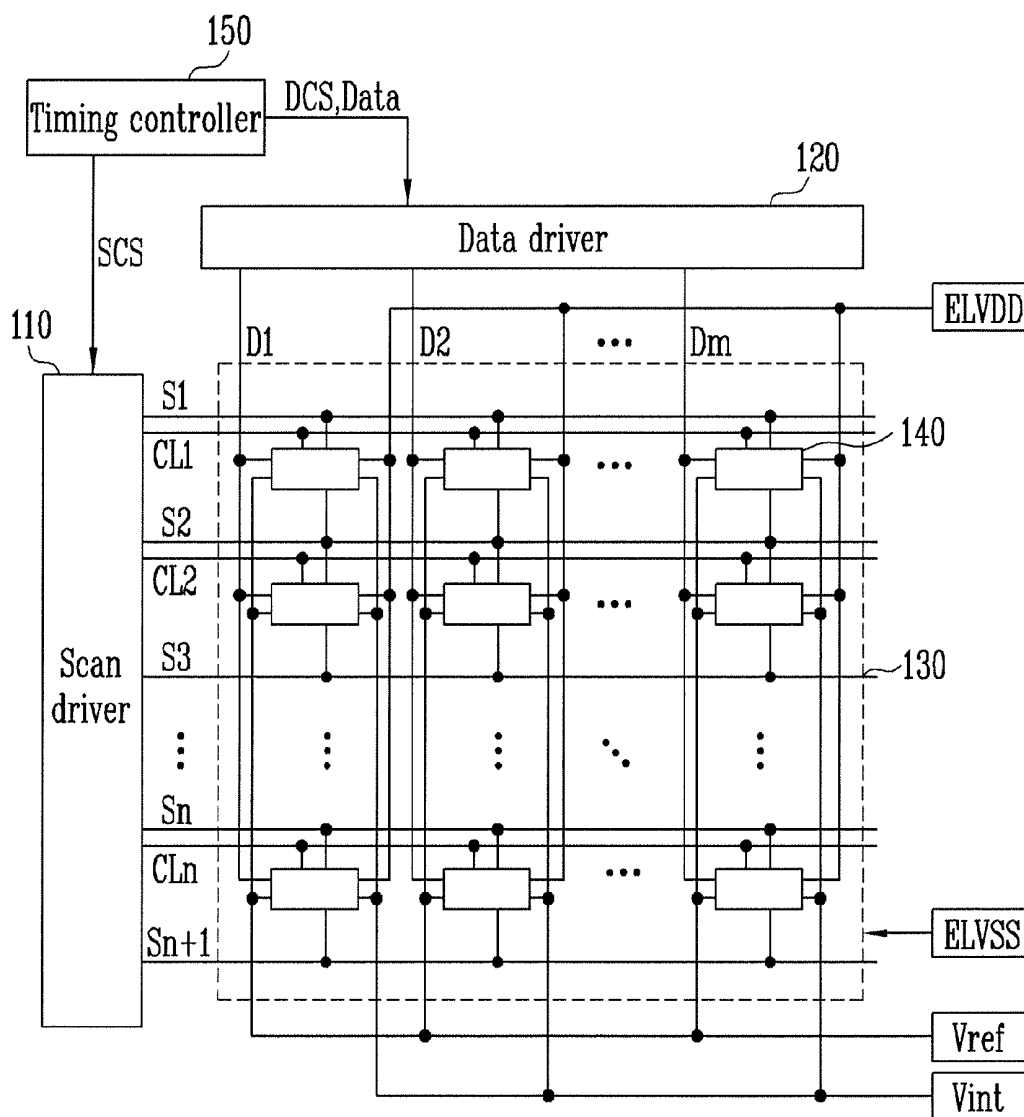


FIG. 1
(PRIOR ART)

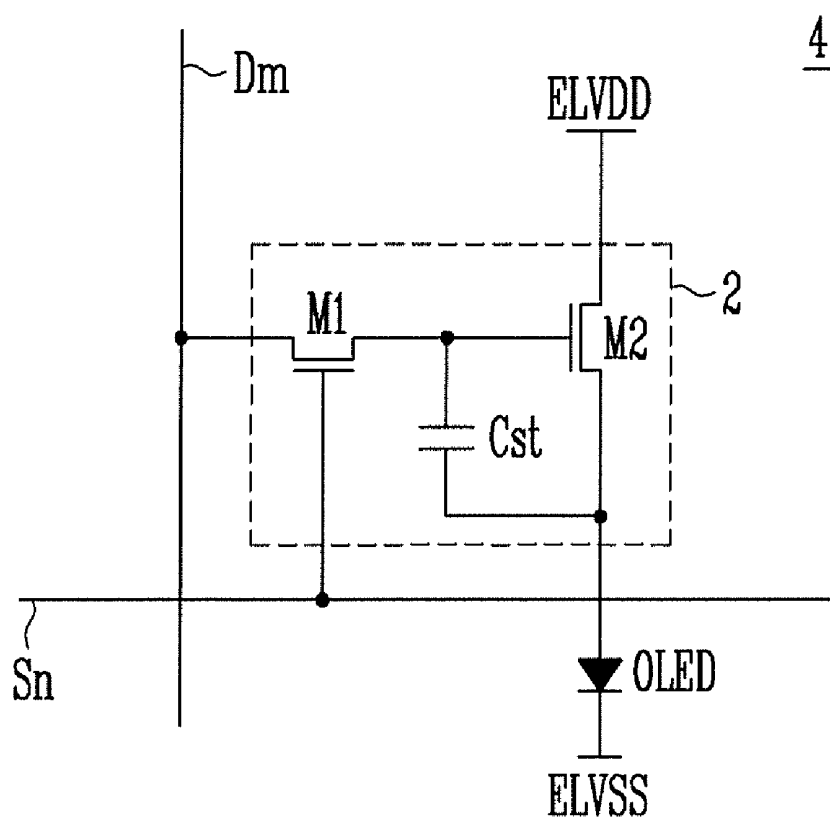


FIG. 2

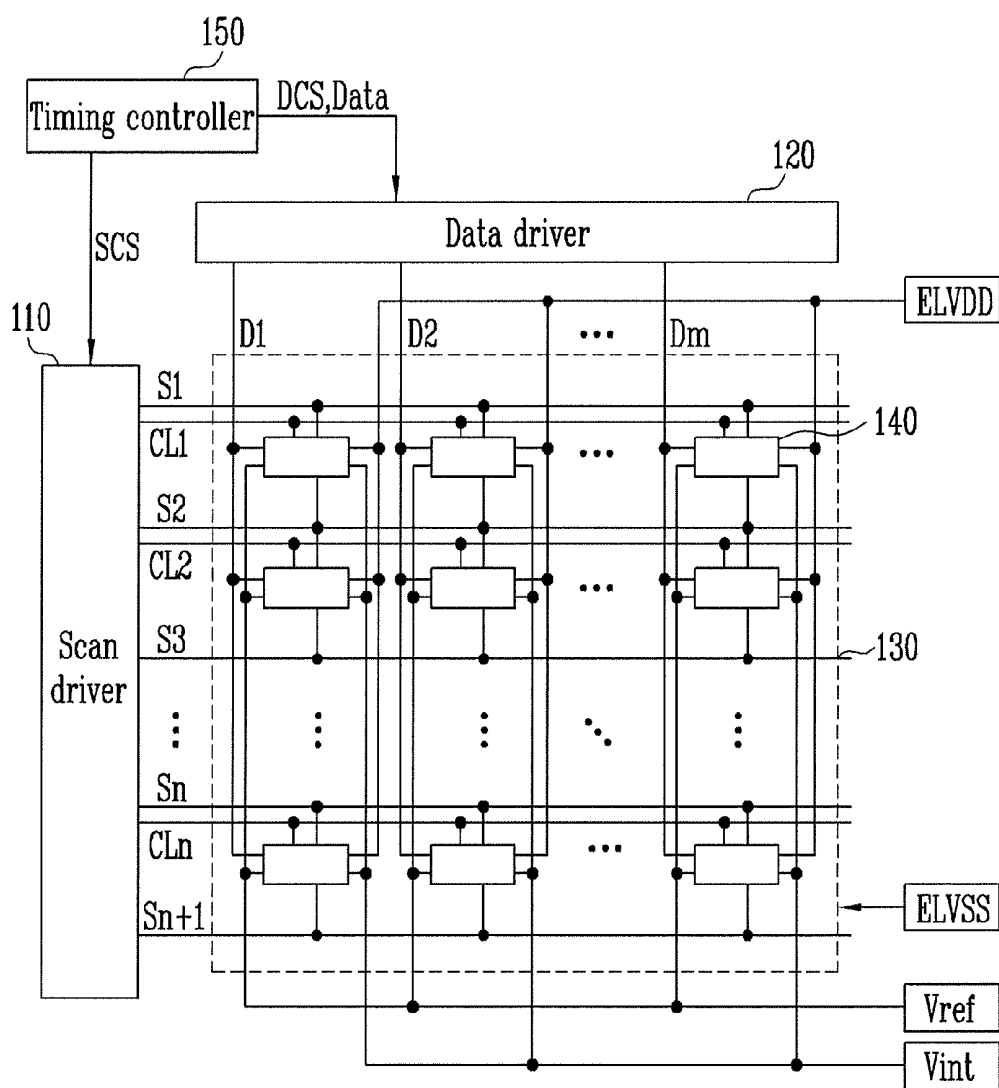


FIG. 3

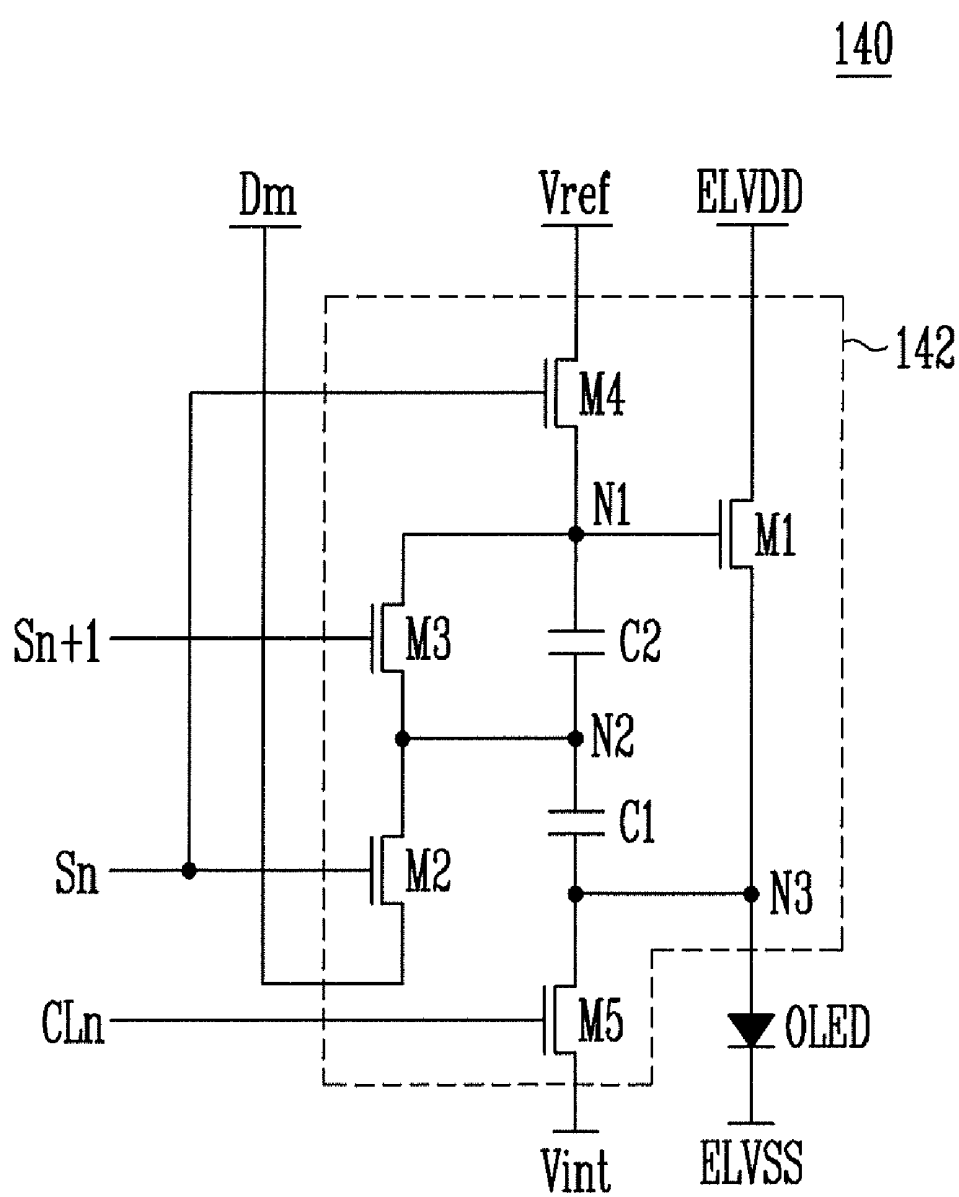


FIG. 4

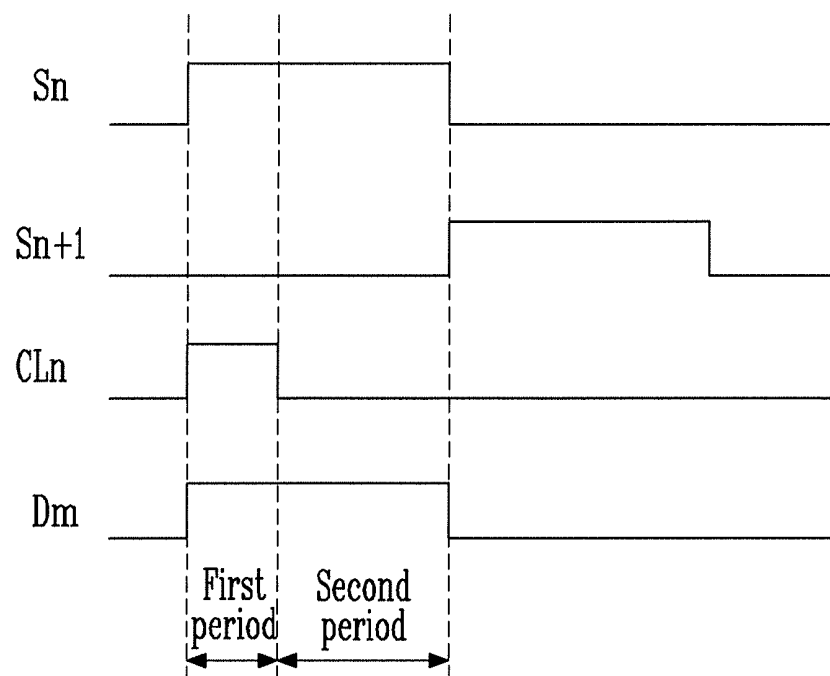
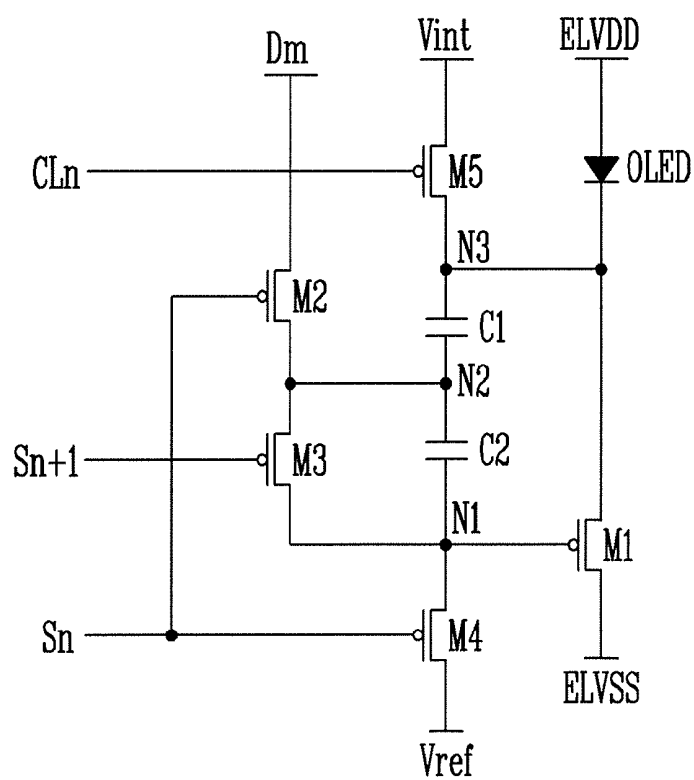


FIG. 5



PIXEL AND ORGANIC LIGHT EMITTING DISPLAY DEVICE USING THE PIXEL

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] This application claims priority to and the benefit of Korean Patent Application No. 10-2009-0033571, filed on Apr. 17, 2009, in the Korean Intellectual Property Office, the entire content of which is incorporated herein by reference.

BACKGROUND

[0002] 1. Field

[0003] The following description relates to a pixel and an organic light emitting display device using the pixel.

[0004] 2. Description of Related Art

[0005] Recently, various flat panel display devices having reduced weight and volume, as compared to cathode ray tubes, have been developed. Among these flat panel display devices, are liquid crystal display devices, field emission display devices, plasma display panels, and organic light emitting display devices, among others.

[0006] Among these flat panel display devices, the organic light emitting display device displays an image using organic light emitting diodes that generate light by the recombination of electrons and holes. Such an organic light emitting display device is driven at low power consumption and has rapid response times.

[0007] FIG. 1 is a circuit view showing a pixel of a conventional organic light emitting display device. In FIG. 1, transistors included in the pixel are NMOS transistors.

[0008] Referring to FIG. 1, the pixel 4 of the conventional organic light emitting display device includes an organic light emitting diode OLED, and a pixel circuit 2 that is coupled to a data line Dm and a scan line Sn to control the organic light emitting diode OLED.

[0009] The anode electrode of the organic light emitting diode OLED is coupled to the pixel circuit 2, and the cathode electrode thereof is coupled to a second power supply ELVSS. Such an organic light emitting diode OLED generates light having a predetermined brightness corresponding to current supplied from the pixel circuit 2.

[0010] The pixel circuit 2 controls an amount of current supplied to the organic light emitting diode OLED corresponding to the data signal supplied to the data line Dm when a scan signal is supplied to the scan line Sn. The pixel circuit 2 includes a second transistor M2 coupled between a first power supply ELVDD and the organic light emitting diode OLED, a first transistor M1 coupled between the second transistor M2 and the data line Dm and having a gate electrode coupled to the scan line Sn, and a storage capacitor coupled between the gate electrode and the first electrode of the second transistor M2.

[0011] The gate electrode of the first transistor M1 is coupled to the scan line Sn, and the first electrode thereof is coupled to the data line Dm. The second electrode of the first transistor M1 is coupled to one terminal of the storage capacitor Cst. Here, the first electrode is either a source electrode or a drain electrode, and the second electrode is the other of the source electrode or the drain electrode. For example, if the first electrode is a drain electrode, the second electrode is a source electrode. When the scan signal is supplied from the scan line Sn, the first transistor M1 coupled the data line Dm is turned on to supply the data signal from the data line Dm to

the storage capacitor Cst. At this time, the storage capacitor Cst is charged with a voltage corresponding to the data signal.

[0012] The gate electrode of the second transistor M2 is coupled to one terminal of the storage capacitor Cst, and the first electrode thereof is coupled to the first power supply ELVDD. The second electrode of the second transistor M2 is coupled to the other terminal of the storage capacitor Cst and the anode electrode of the organic light emitting diode OLED. The second transistor M2 controls the amount of current flowing from the first power supply ELVDD to the second power supply ELVSS via the organic light emitting diode OLED corresponding to the voltage stored in the storage capacitor Cst.

[0013] One terminal of the storage capacitor Cst is coupled to the gate electrode of the second transistor M2, and the other terminal thereof is coupled to the anode electrode of the organic light emitting diode OLED. The storage capacitor Cst is charged with a voltage corresponding to the data signal.

[0014] The conventional pixel as described above displays an image having a predetermined brightness by supplying current corresponding to the voltage charged in the storage capacitor Cst to the organic light emitting diode OLED. However, the conventional organic light emitting display device cannot display an image having a uniform brightness due to deviations of the threshold voltages of the second transistors M2 in different pixels of the display device.

[0015] When the respective pixels 4 have different threshold voltages, the respective pixels 4 generate light having different brightness despite receiving a same data signal, such that an image having a uniform or desired brightness is difficult to display.

SUMMARY OF THE INVENTION

[0016] Therefore, exemplary embodiments of the present invention provide a pixel for displaying an image having a uniform brightness, irrespective of the threshold voltage of the driving transistor of the pixel, and an organic light emitting display device using the pixel.

[0017] According to an exemplary embodiment of the present invention, there is provided a pixel, including: an organic light emitting diode having a cathode electrode coupled to a second power supply; a first transistor for controlling an amount of current flowing from a first power supply to the second power supply through the organic light emitting diode; a second transistor coupled to a data line and turned on when a scan signal is supplied to an i^{th} (i is a natural number) scan line; a third transistor coupled between the second transistor and a gate electrode of the first transistor and turned on when a scan signal is supplied to an $i+1^{th}$ scan line; a fourth transistor coupled between the gate electrode of the first transistor and a reference power supply and turned on when the scan signal is supplied to the i^{th} scan line; a fifth transistor coupled between an anode electrode of the organic light emitting diode and an initial power supply and is turned on when a control signal is supplied to a control line; a first capacitor coupled between the anode electrode of the organic light emitting diode and a node between the second transistor and the third transistor; and a second capacitor coupled between the node and the gate electrode of the first transistor.

[0018] The fifth transistor may be turned on during a portion of a time period when the second transistor is turned on. The fifth transistor may be turned on concurrently with the second transistor. The reference power supply may have a voltage greater than a voltage of the initial power supply.

[0019] According to another exemplary embodiment of the present invention, there is provided an organic light emitting display, including: a scan driver for supplying scan signals sequentially to scan lines and supplying control signals sequentially to control lines; a data driver for supplying data signals to data lines in accordance with the scan signals; and pixels at crossing regions of the scan lines, the control lines and the data lines, wherein a pixel of the pixels positioned at an i^{th} (i is a natural number) scan line of the scan lines includes: an organic light emitting diode having a cathode electrode coupled to a second power supply; a first transistor for controlling an amount of current flowing from a first power supply to the second power supply through the organic light emitting diode; a second transistor coupled to a data line of the data lines and turned on when the scan signal is supplied to the i^{th} scan line; a third transistor coupled between the second transistor and a gate electrode of the first transistor and turned on when a scan signal is supplied to an $i+1^{th}$ scan line; a fourth transistor coupled between the gate electrode of the first transistor and a reference power supply and turned on when the scan signal is supplied to the i^{th} scan line; a fifth transistor coupled between an anode electrode of the organic light emitting diode and an initial power supply and turned on when the control signal is supplied to an i^{th} control line of the control lines; a first capacitor coupled between the anode electrode of the organic light emitting diode and a node between the second transistor and the third transistor; and a second capacitor coupled between the node and the gate electrode of the first transistor.

[0020] A voltage of a data signal supplied to the data line may be greater than or equal to a voltage of the reference power supply. The initial power supply may have a voltage lower than a voltage obtained by subtracting a threshold voltage of the first transistor from a voltage of the reference power supply. The initial power supply may be set at a voltage for turning the organic light emitting diode off. The scan driver may be configured to supply the control signal to the i^{th} control line during a portion of a time period when the scan signal is supplied to the i^{th} scan line. The scan driver may be configured to supply the control signal to the i^{th} control line concurrently with the scan signal supplied to the i^{th} scan line.

[0021] With the pixel and the organic light emitting display device using the pixel according to exemplary embodiments of the present invention, an image having a uniform or desired brightness can be displayed, irrespective of deviations in the threshold voltages of the driving transistors of different pixels in the display device.

BRIEF DESCRIPTION OF THE DRAWINGS

[0022] The accompanying drawings, together with the specification, illustrate exemplary embodiments of the present invention, and, together with the description, serve to explain the principles of the present invention.

[0023] FIG. 1 is a circuit view showing a conventional pixel;

[0024] FIG. 2 is a schematic diagram showing an organic light emitting display device according to an embodiment of the present invention;

[0025] FIG. 3 is a circuit diagram showing an embodiment of a pixel of FIG. 2;

[0026] FIG. 4 is a waveform view showing a driving method of the pixel of FIG. 3; and

[0027] FIG. 5 is a circuit diagram showing another embodiment of a pixel of FIG. 2.

DETAILED DESCRIPTION OF EXEMPLARY EMBODIMENTS

[0028] Hereinafter, certain exemplary embodiments according to the present invention will be described, with reference to the accompanying drawings. Here, when a first element is described as being coupled to a second element, the first element may be directly coupled to the second element, or may be indirectly coupled to the second element via one or more additional elements. Further, some of the elements that are not essential to the complete understanding of the invention are omitted for clarity. Also, like reference numerals refer to like elements throughout.

[0029] Hereinafter, exemplary embodiments of the present invention will be described in more detail with reference to the accompanying FIGS. 2 to 5.

[0030] FIG. 2 is a schematic diagram showing an organic light emitting display device according to an embodiment of the present invention.

[0031] Referring to FIG. 2, the organic light emitting display device according to the embodiment of the present invention includes pixels 140 that are respectively coupled to scan lines S1 to Sn+1, control lines CL1 to CLn, and data lines D1 to Dm, a scan driver 110 for driving the scan lines S1 to Sn+1 and the control lines CL1 to CLn, a data driver 120 for driving the data lines D1 to Dm, and a timing controller 150 for controlling the scan driver 110 and the data driver 120.

[0032] The scan driver 110 receives a scan driving control signal SCS from the timing controller 150. The scan driver 110 generates scan signals and supplies the scan signals sequentially to the scan lines S1 to Sn+1. Also, the scan driver 110 generates control signals and supplies the control signals sequentially to the control lines CL1 to CLn. Here, the control signals overlap with the scan signals during a first period, or a first portion, of a time period when the scan signals are supplied. For example, the control signal is supplied to an i^{th} (i is a natural number) control line CLi during the first period of the time period when the scan signal is supplied to the i^{th} scan line Si. The control signal has a voltage having a same polarity (for example, a high level voltage) as the scan signal.

[0033] The data driver 120 receives a data driving control signal DCS from the timing controller 150. The data driver 120 supplies the data signals to the data lines D1 to Dm synchronously with the scan signals.

[0034] The timing controller 150 generates a data driving control signal DCS and a scan driving control signal SCS corresponding to synchronization signals supplied from the outside. The data driving control signal DCS is supplied to the data driver 120 and the scan driving control signal SCS is supplied to the scan driver 110. The timing controller 150 also supplies data supplied from the outside to the data driver 120.

[0035] A display region 130 receives a first power ELVDD, a second power ELVSS, a reference voltage Vref, and an initial power Vint from the outside, to be supplied to the respective pixels 140. The respective pixels 140 receive the first power ELVDD, the second power ELVSS, the reference voltage Vref, and the initial power Vint, and generate light corresponding to the data signals.

[0036] Here, the first power ELVDD, the voltage Vdata corresponding to the data signal, and the voltages of the

reference power supply V_{ref} and the initial power supply V_{int} are set in accordance with the following equation 1.

Equation 1

$$ELVDD > V_{data} \quad V_{ref} \geq V_{int}$$

[0037] Referring to equation 1, the reference power V_{ref} is set as a voltage equal to or lower than the voltage V_{data} corresponding to the data signal. The initial power V_{int} is set as a voltage lower than the reference power V_{ref} . More precisely the initial power V_{int} is set as a voltage lower than the voltage obtained by subtracting the threshold voltage of the driving transistor from the reference voltage V_{ref} . Meanwhile, although not included in equation 1, the second power $ELVSS$ is set as a low voltage for current to flow from the first power $ELVDD$ through the organic light emitting diode OLED. For example, the power supply $ELVSS$ is set as a voltage lower than the reference power V_{ref} .

[0038] Meanwhile, the pixel 140 positioned on the i^{th} (i is a natural number) horizontal line is coupled to the i^{th} scan line S_i , the i^{th} control line CL_i , and the $i+1^{th}$ scan line S_{i+1} . The pixel 140 according to the embodiment includes a plurality of NMOS-type transistors, and supplies current that compensates for the threshold voltage of the driving transistor to the organic light emitting diode.

[0039] FIG. 3 is a circuit diagram showing an embodiment of a pixel of FIG. 2. For convenience of explanation, in FIG. 3, a pixel 140 positioned on an n^{th} horizontal line and coupled to an m^{th} data line D_m will be described.

[0040] Referring to FIG. 3, the pixel 140 according to the embodiment of the present invention includes an organic light emitting diode OLED, and a pixel circuit 142 that is coupled to the data line D_m , scan lines S_n and S_{n+1} , and a control line CL_n for controlling the organic light emitting diode OLED.

[0041] The anode electrode of the organic light emitting diode OLED is coupled to the pixel circuit 142, and the cathode electrode thereof is coupled to a second power supply $ELVSS$. Such an organic light emitting diode OLED generates light having a brightness (e.g., a predetermined brightness) corresponding to a current supplied from the pixel circuit 142.

[0042] The pixel circuit 142 is charged with a voltage corresponding to the data signal supplied to the m^{th} data line D_m when a scan signal is supplied to the n^{th} scan line S_n , and the threshold voltage of a first transistor, and supplies current corresponding to the charged voltage to the organic light emitting diode OLED when a scan signal is supplied to the $n+1^{th}$ scan line S_{n+1} . To this end, the pixel circuit 142 includes first to fifth transistors M1 to M5, a first capacitor C1 and a second capacitor C2.

[0043] A gate electrode of the first transistor M1 is coupled to a first node N1, a first electrode thereof is coupled to a first power supply $ELVDD$, and a second electrode thereof is coupled to the anode electrode of the organic light emitting diode OLED (at a third node N3). The first transistor M1 controls an amount of current supplied to the organic light emitting diode OLED corresponding to the voltage applied to the first node N1.

[0044] The gate electrode of the second transistor M2 is coupled to the n^{th} scan line S_n , the first electrode thereof is coupled to the m^{th} data line D_m , and the second electrode thereof is coupled to a second node N2. The second transistor M2 is turned on when the scan signal is supplied to the scan line S_n to electrically couple the data line D_m to the second node N2.

[0045] The gate electrode of the third transistor M3 is coupled to the $n+1^{th}$ scan line S_{n+1} , the first electrode thereof is coupled to the second node N2, and the second electrode thereof is coupled to the first node N1 (that is, the gate electrode of the first transistor M1). The third transistor M3 is turned on when the scan signal is supplied to the $n+1^{th}$ scan line S_{n+1} to electrically couple the first node N1 to the second node N2.

[0046] The gate electrode of the fourth transistor M4 is coupled to the n^{th} scan line S_n , the first electrode thereof is coupled to the reference power supply V_{ref} , and the second electrode thereof is coupled to the first node N1. The fourth transistor M4 is turned on when the scan signal is supplied to the n^{th} scan line S_n to supply the voltage of the reference power supply V_{ref} to the first node N1.

[0047] The gate electrode of the fifth transistor M5 is coupled to the n^{th} control line CL_n , the first electrode thereof is coupled to the third node N3, and the second electrode thereof is coupled to the initial power supply V_{int} . The fifth transistor M5 is turned on when the control signal is supplied to the n^{th} control line CL_n to supply the initial power V_{int} to the third node N3.

[0048] The first capacitor C1 and the second capacitor C2 are coupled between the first node N1 and the third node N3 in series. The common node between the first capacitor C1 and the second capacitor C2 is coupled to the common node between the second transistor M2 and the third transistor M3 (that is, the second node N2). Here, the second capacitor C2 and the third transistor M3 are coupled between the first node N1 and the second node N2 in parallel.

[0049] FIG. 4 is a waveform view showing a driving method of the pixel of FIG. 3.

[0050] Explaining the operating process of the pixel 140 of the embodiment in detail in reference to FIGS. 3 and 4, first the scan signal is supplied to the n^{th} scan line S_n and the control signal is supplied to the control line CL_n during a first period, or a first portion, of a time period when the scan signal is supplied to the scan line S_n .

[0051] When the scan signal is supplied to the scan line S_n , the second transistor M2 and the fourth transistor M4 are turned on. When the second transistor M2 is turned on, the data signal is supplied from the data line D_m to the second node N2. When the fourth transistor M4 is turned on, the reference power V_{ref} is supplied to the first node N1.

[0052] When the control signal is supplied to the control line CL_n , the fifth transistor M5 is turned on. When the fifth transistor M5 is turned on, the initial power is supplied to the third node N3. Here, the initial power V_{int} is set as a voltage that allows the organic light emitting diode OLED to be turned off. Accordingly, light is not generated from the organic light emitting diode OLED during this period.

[0053] Thereafter, the supply of the control signal to the control line CL_n is stopped for a second period or second portion of the time period when the scan signal is supplied to the scan line S_n . When the supply of the control signal to the control line CL_n is stopped, the fifth transistor M5 is turned off. When the fifth transistor M5 is turned off, the voltage of the third node N3 is raised to the voltage obtained by subtracting the threshold voltage of the first transistor M1 from the voltage of the reference power supply V_{ref} .

[0054] More specifically, during the first period of the time period when the scan signal is supplied to the scan line S_n , the voltage of the first node N1 is set to the reference power V_{ref} and the voltage of the third node N3 is set to the initial power

V_{int}. Here, the voltage of the initial power supply V_{int} is set as a voltage lower than the voltage obtained by subtracting the threshold voltage of the first transistor M1 from the voltage of the reference power supply V_{ref}. Then, when the fifth transistor M5 is turned off, the voltage of the third node N3 is raised to the voltage obtained by subtracting the threshold voltage of the first transistor M1 from the voltage of the reference power supply V_{ref}.

[0055] In this case, the second capacitor C2 between the second node N2 and the first node N1 is charged with a voltage V_{data}-V_{ref}, and the first capacitor C1 between the second node N2 and the third node N3 is charged with a voltage V_{data}-V_{ref}+V_{th}(M1).

[0056] Thereafter, the supply of the scan signal to the n^{th} scan line S_n is stopped so that the second transistor M2 and the fourth transistor M4 are turned off, and a scan signal is supplied to the $n+1^{th}$ scan line S_{n+1} so that the third transistor M3 is turned on. When the third transistor M3 is turned on, the first node N1 is coupled electrically to the second node N2. In this case, a voltage across the second capacitor C2 is set to 0, and the voltage V_{gs}(M1) between the gate electrode and the source electrode of the first transistor M1 is equal to the voltage charged in the first capacitor C1. In other words, the voltage between the gate electrode and the source electrode of the first transistor M1 is set by the following equation 2.

Equation 2

$$V_{gs}(M1) = V_{data} - V_{ref} + V_{th}(M1)$$

[0057] The amount of current flowing to the organic light emitting diode OLED corresponds to the voltage V_{gs} of the first transistor M1 in accordance with the following equation 3.

Equation 3

$$I_{oled} = \beta \{ (V_{gs}(M1) - V_{th}(M1))^2 \} = \beta \{ (V_{data} - V_{ref} + V_{th}(M1) - V_{th}(M1))^2 \} = \beta (V_{data} - V_{ref})^2$$

[0058] Referring to equation 3, the current flowing to the organic light emitting diode OLED is determined according to a voltage difference between the voltage V_{data} of the data signal and the reference power V_{ref}. Here, the reference power V_{ref} is a fixed voltage, so that the current flowing to the organic light emitting diode OLED is determined by the data signal. In other words, in the embodiment of the present invention, an image having a uniform brightness can be displayed, irrespective of deviations in the threshold voltages of the first transistors M1 of different pixels.

[0059] Meanwhile, although NMOS transistors are shown in FIG. 3, the present invention is not limited thereto. For example, the NMOS transistors in FIG. 3 may be changed to PMOS transistors as shown in FIG. 5. In this case, the polarity of the waveforms shown in FIG. 4 is inverted, and supplied having substantially the same operating process.

[0060] While the present invention has been described in connection with certain exemplary embodiments, it is to be understood that the invention is not limited to the disclosed embodiment, but is instead intended to cover various modifications and equivalent arrangements included within the spirit and scope of the appended claims, and equivalents thereof.

1. A pixel, comprising:

an organic light emitting diode having a cathode electrode coupled to a second power supply;

a first transistor for controlling an amount of current flowing from a first power supply to the second power supply through the organic light emitting diode;

a second transistor coupled to a data line and turned on when a scan signal is supplied to an i^{th} (i is a natural number) scan line;

a third transistor coupled between the second transistor and a gate electrode of the first transistor and turned on when a scan signal is supplied to an $i+1^{th}$ scan line;

a fourth transistor coupled between the gate electrode of the first transistor and a reference power supply and turned on when the scan signal is supplied to the i^{th} scan line;

a fifth transistor coupled between an anode electrode of the organic light emitting diode and an initial power supply and turned on when a control signal is supplied to a control line;

a first capacitor coupled between the anode electrode of the organic light emitting diode and a node between the second transistor and the third transistor; and

a second capacitor coupled between the node and the gate electrode of the first transistor.

2. The pixel as claimed in claim 1, wherein the fifth transistor is turned on during a portion of a time period when the second transistor is turned on.

3. The pixel as claimed in claim 2, wherein the fifth transistor is turned off during a remaining portion of the time period when the second transistor is turned on.

4. The pixel as claimed in claim 2, wherein the fifth transistor is turned on concurrently with the second transistor.

5. The pixel as claimed in claim 1, wherein the reference power supply has a voltage greater than a voltage of the initial power supply.

6. The pixel as claimed in claim 1, wherein a data signal is supplied to the data line when the scan signal is supplied to the i^{th} scan line, a voltage of the data signal being greater than or equal to a voltage of the reference power supply.

7. An organic light emitting display, comprising:

a scan driver for supplying scan signals sequentially to scan lines and supplying control signals sequentially to control lines;

a data driver for supplying data signals to data lines in accordance with the scan signals; and

pixels at crossing regions of the scan lines, the control lines and the data lines,

wherein a pixel of the pixels positioned at an i^{th} (i is a natural number) scan line of the scan lines comprises:

an organic light emitting diode having a cathode electrode coupled to a second power supply;

a first transistor for controlling an amount of current flowing from a first power supply to the second power supply through the organic light emitting diode;

a second transistor coupled to a data line of the data lines and turned on when the scan signal is supplied to the i^{th} scan line;

a third transistor coupled between the second transistor and a gate electrode of the first transistor and turned on when a scan signal is supplied to an $i+1^{th}$ scan line of the scan lines;

a fourth transistor coupled between the gate electrode of the first transistor and a reference power supply and turned on when the scan signal is supplied to the i^{th} scan line;

a fifth transistor coupled between an anode electrode of the organic light emitting diode and an initial power supply and turned on when the control signal is supplied to an i^{th} control line of the control lines;

a first capacitor coupled between the anode electrode of the organic light emitting diode and a node between the second transistor and the third transistor; and

a second capacitor coupled between the node and the gate electrode of the first transistor.

8. The organic light emitting display as claimed in claim 7, wherein a voltage of a data signal supplied to the data line is greater than or equal to a voltage of the reference power supply.

9. The organic light emitting display as claimed in claim 7, wherein the initial power supply has a voltage lower than a voltage obtained by subtracting a threshold voltage of the first transistor from a voltage of the reference power supply.

10. The organic light emitting display as claimed in claim 9, wherein the initial power supply is set at a voltage for turning the organic light emitting diode off.

11. The organic light emitting display as claimed in claim 7, wherein the scan driver is configured to supply the control signal to the i^{th} control line during a portion of a time period when the scan signal is supplied to the i^{th} scan line.

12. The organic light emitting display as claimed in claim 11, wherein the control signal is not supplied to the control line during a remaining portion of the time period when the scan signal is supplied to the i^{th} scan line.

13. The organic light emitting display as claimed in claim 11, wherein the scan driver is configured to supply the control signal to the i^{th} control line concurrently with the scan signal supplied to the i^{th} scan line.

* * * * *

专利名称(译)	使用该像素的像素和有机发光显示装置		
公开(公告)号	US20100265166A1	公开(公告)日	2010-10-21
申请号	US12/693388	申请日	2010-01-25
[标]申请(专利权)人(译)	康CHUL KYU 崔俞廉 KIM KEUM NAM		
申请(专利权)人(译)	康喆KYU 蔡俞莲 KIM KEUM-NAM		
当前申请(专利权)人(译)	康喆KYU 蔡俞莲 KIM KEUM-NAM		
[标]发明人	KANG CHUL KYU CHOI YU LIAN KIM KEUM NAM		
发明人	KANG, CHUL-KYU CHOI, YU-LIAN KIM, KEUM-NAM		
IPC分类号	G09G3/30		
CPC分类号	G09G3/3233 G09G2300/0852 G09G2320/043 G09G2310/0262 G09G2320/0233 G09G2300/0861		
优先权	1020090033571 2009-04-17 KR		
其他公开文献	US8907870		
外部链接	Espacenet USPTO		

摘要(译)

像素包括有机发光二极管;第一晶体管;第二晶体管,其耦合到数据线,并且当扫描信号被提供给第i扫描线时导通;第三晶体管位于第二晶体管和第一晶体管的栅极之间,当扫描信号提供给第i+1扫描线时,第三晶体管导通;第四晶体管,位于第一晶体管的栅极和参考电源之间,当扫描信号提供给第i扫描线时导通;在有机发光二极管和初始电源之间的第五晶体管,当提供控制信号时导通;有机发光二极管与第二晶体管和第三晶体管之间的节点之间的第一电容器;以及第一晶体管的节点和栅电极之间的第二电容器。

